

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,954,097 B2
APPLICATION NO. : 09/758970
DATED : October 11, 2005
INVENTOR(S) : Ronnie M. Harrison

Page 1 of 2

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

<u>Column, Line</u>	<u>Reads</u>	<u>Should Read</u>
On Title Page		
Item (56), References	“5,444,567 A 8/1995	--5,444,667 8/1995
Cited, U.S. Patent Documents	Kataoka.....365/233”	Obara.....365/233--
On Title Page		
Item (56), References	“”Fast Acquisition	“Fast Acquisition
Cited, Other Publications,	Frequency Synthesizer	Frequency Synthesizer
Park Reference	with the Multiple Phase Detectors”,”	with Multiple Phase Detectors,”--
Column 4, Line 34	“and generating the second”	--and generates the second--
Column 4, Lines 46-47	“embodiment of clock generator in accordance with present invention”	--embodiment of the clock generator in accordance with the present invention--
Column 4, Line 52	“with present invention.”	--with the present invention.--
Column 4, Line 62	“in accordance with”	--in accordance with the--
Column 6, Line 16	“illustrated in FIG. 3. there”	--illustrated in FIG. 3 there--
Column 7, Line 2	“to a phase detector”	--a phase detector--
Column 7, Line 19	“delay of second signal”	--delay of the second signal--
Column 8, Line 67	“or a Peripheral component”	--or a peripheral component--
Column 9, Line 1	“operator or an electronic”	--operator of an electronic--

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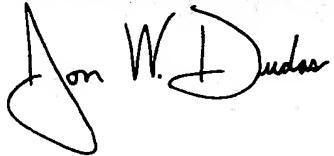
Page 2 of 2

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 9, Line 12	“disk drives floppy disk”	-- disk drives, floppy disk --
Column 11, Line 39	“first clock signals”	--first clock signal--

Signed and Sealed this

Nineteenth Day of December, 2006



JON W. DUDAS
Director of the United States Patent and Trademark Office